FIG.1

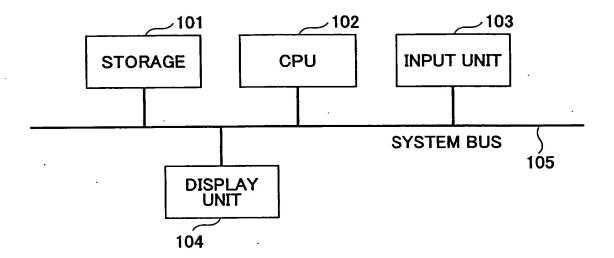
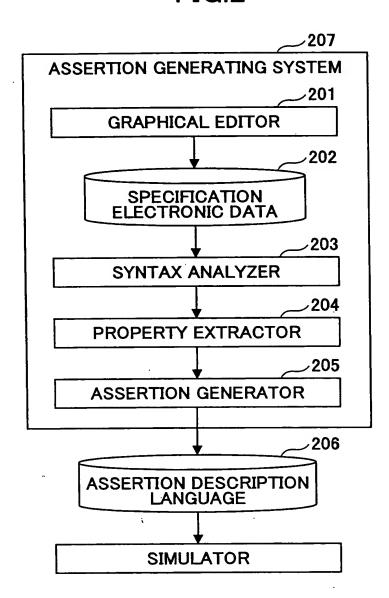
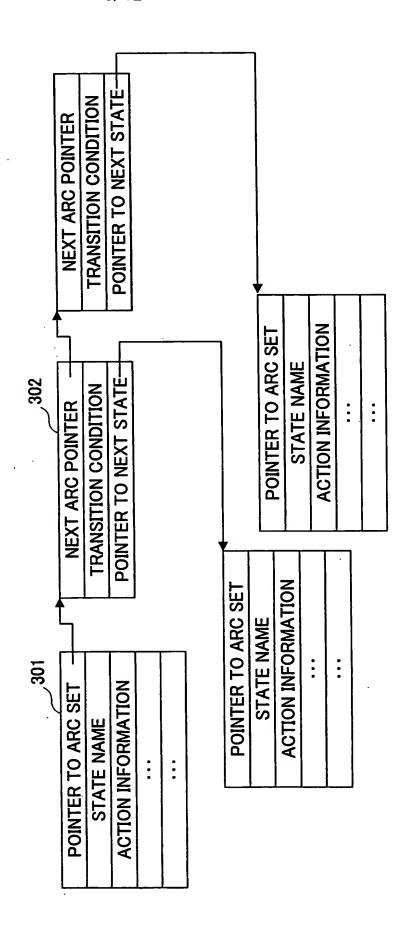


FIG.2







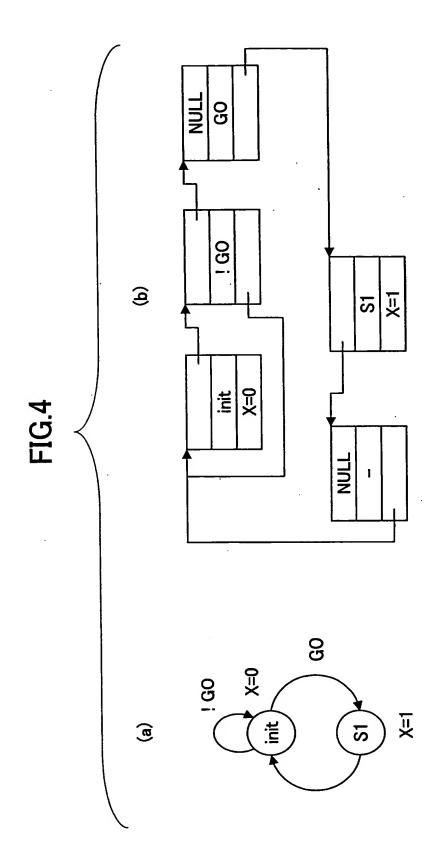


FIG.5

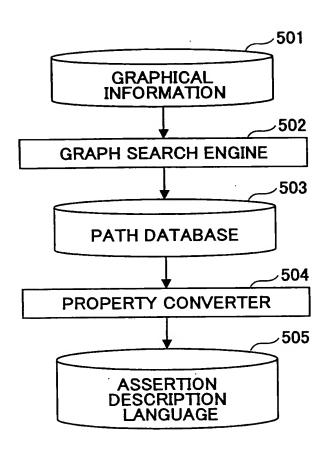


FIG.6

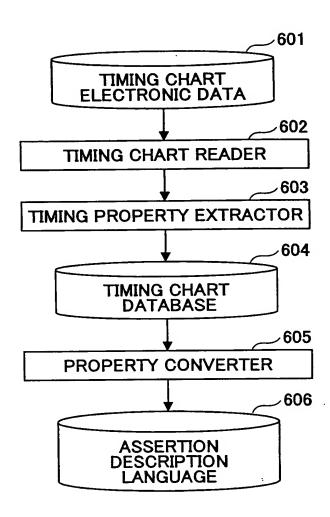


FIG.7

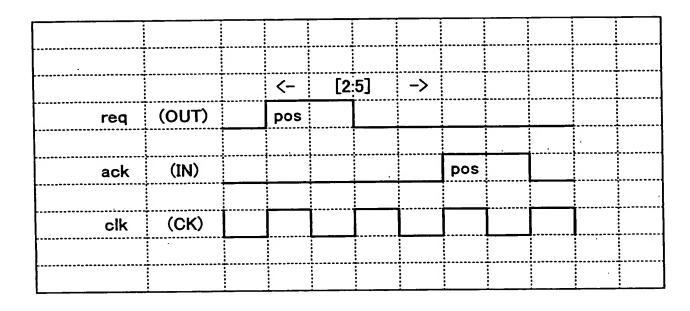


FIG.8

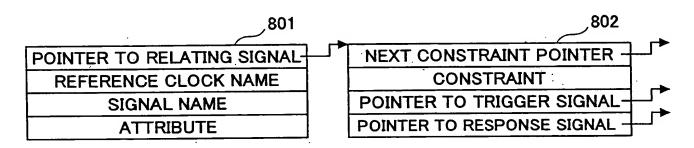
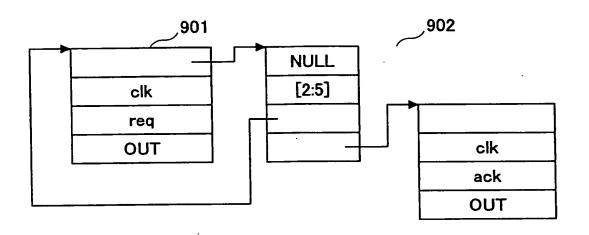
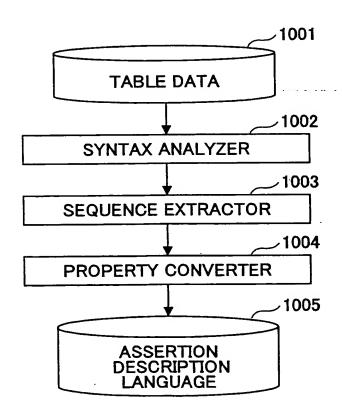


FIG.9



**FIG.10** 



**FIG.11** 

NAME	FADD			
CLOCK	posedge clk			
CONDITION	CONDITION	CONDITION	EXPECT	<u>EXPECT</u>
A	<u>B</u>	<u>CI</u>	<u>S</u>	<u>co</u>
<u>0</u>	<u>0</u>	· <u>0</u>	<u>0</u>	<u>0</u>
<u>0</u>	<u>0</u>	1	· <u>1</u>	<u>0</u>
<u>0</u>	1	<u>0</u>	<u>1</u>	<u>0</u>
<u>0</u>	1	1	<u>0</u>	1
1	<u>0</u>	<u>0</u>	1	<u>0</u>
1	<u>o</u>	1	<u>0</u>	1
1	1	<u>0</u>	<u>o</u>	1
1	1	11	1	<u>1</u>

## FIG.12

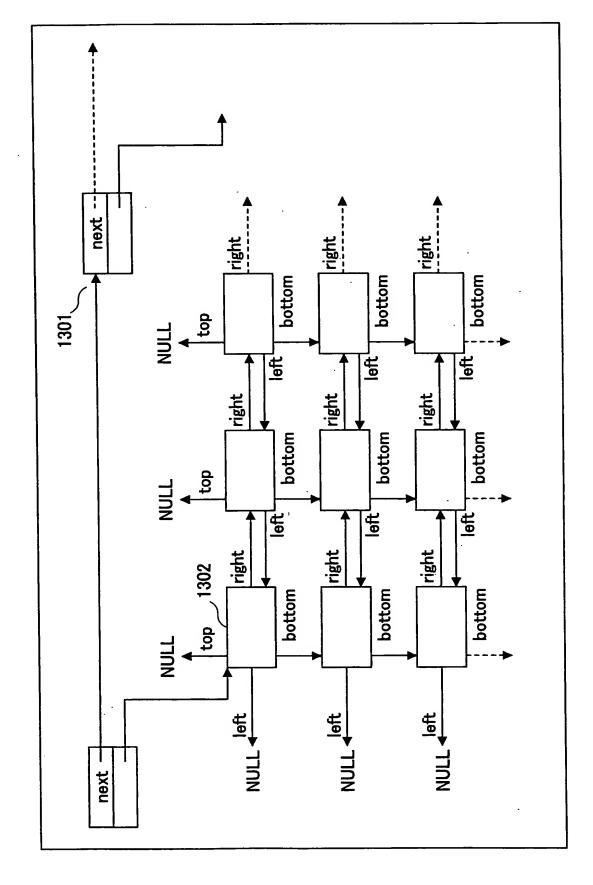


FIG.13

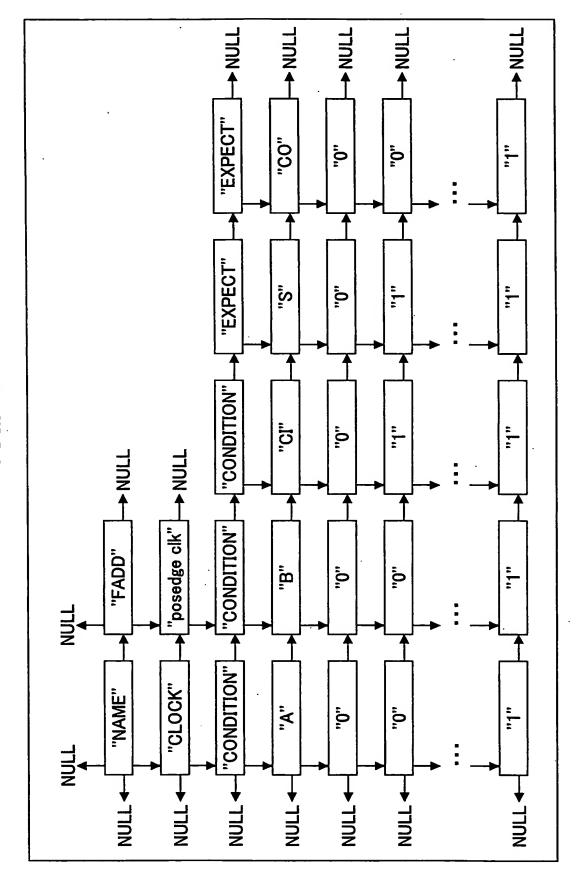


FIG.14

12/12

## FIG.15

NAME	<u>reqack</u>	
<u>CLOCK</u>	posedge clk	
CONDITION	EXPECT	
req	<u>ack</u>	
rose	<u>0[*2:5];1;0</u>	